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INFORMATION DISCLOSURE CITATION

Applicants:

Söderquist et al.

Examiner:

Robert E. Fennema

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Group Art Unit:

2183

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	REF. NO.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS
/RF/	AA	6,308,197 B1	10/23/01	Mason et al.	709/102	709/318
	AB					
	AC					
	AD					
	AE					
	AF					

FOREIGN PATENT DOCUMENTS

	REF. NO.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
	CA						
	CB						
	CC						
	CD						

OTHER REFERENCES

	REF. NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
/RF/	CE	Gerndt R. et al. "An event-driven multi-threading architecture for embedded systems"; Hardware/Software CoDesign, 1997. Proceedings of the Fifth International Workshop on Braunschweig, Germany 24-26 March 1997 pages 29-33.
/RF/	CF	Zoran Salcic, et al. "Reflex: A Processor Core for Reactive Embedded Applications"; Mainstream 12th International Conference; Vol. 2438/2002, 2 September 2002; pages 945-954
/RF/	CG	Baganne et al., "Hardware interface design for real time embedded systems" 1997. Proceedings. Seventh Great Lakes Symposium on Urbana-Champaign, IL 13-15 March 1997
	CH	
	CI	
	CJ	
	CK	

Examiner	/Robert Fennema/	Date Considered	05/06/2007
Examiner: Initial if reference consider, whether or not citation is in conformance with MPEP §609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Application			